

(Affiliated to JNTUA Ananthapuramu and approved by AICTE, New Delhi)

2.6.1 Programme Outcomes (POs) and Course Outcomes (COs) for all Programmes offered by the institution are stated and displayed on website and attainment of POs and COs are evaluated

Answer:

Outcome-Based Education (OBE) model is being adopted in this Institution where the Internal Quality Assurance Cell (IQAC) guide and monitor the implementation of OBE in the college, the Quality assurance and improvement process is about determining whether the set educational objectives meet a general standard of quality. Course Outcomes are the statements indicating knowledge and skills the student is expected to acquire at the end of a course.

In strict compliance with the objectives of Outcome Based Education (OBE), Program Specific Outcomes (PSOs) are framed by the department offering the concerned program after rigorous consultation with all faculty and the stakeholders. After attainment of consensus, the same are widely propagated and publicized through various means such as display and/or communication specified hereunder. POs and COs are followed as prescribed by the syllabus framed by the university.

- Website
- Class rooms
- Department Notice Boards
- Laboratories
- Student Induction Programs
- Parent meet
- Faculty meetings
- Alumni meetings
- Library

While addressing the students, the HODs create awareness on POs, PSOs and COs. The faculty members, class teachers, mentors also inform the students and create awareness and emphasize the need to attain the outcomes.

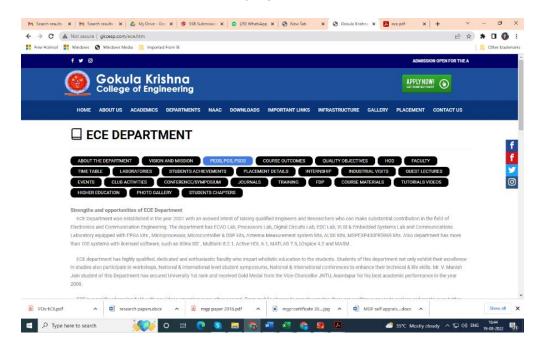
Program specific outcomes (PSOs) are the specific skill requirements and accomplishments to be fulfilled by the students at micro level and by the end of the program. The Head of the Department along with senior faculty prepare the PSOs, discuss with subject experts and approve it after endorsement by the Principal.

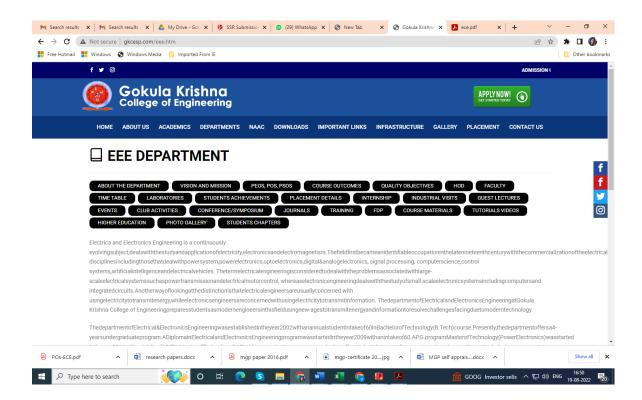
Program Outcomes (POs) are broad statements that describe the professional accomplishments which the program aims at, and these are to be attained by the students by the time they complete the program. POs incorporate many areas of inter-related knowledge, skills and personality traits that are to be acquired by the students during their graduation.

Course outcomes (COs) are direct statements that describe the essential and enduring disciplinary knowledge, abilities that students should possess and the depth of learning that is expected upon completion of a course. They are clearly specified in the syllabus itself and communicated to students properly by the subject teachers. The POs/PSOs of the Programme are published through electronic media at individual Department site located on the college website http://www.gkcesp.com. The COs of the courses are also published through electronic media at the Department site located on the college website. In all the interactions with the students, awareness on POs, PSOs and COs is consciously promoted. Attainment of Program outcomes are evaluated in the project reviews at the end of the program and attainment of course outcomes are evaluated by asking viva in the lab session.



Programme Outcomes (POs) and Course Outcomes (COs) for all Programmes offered by the institution are stated and displayed on website







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List of POs

- **PO1**. **Engineering knowledge**: Apply the knowledge of mathematics, science, engineering fundamentals, and an engineering specialization to the solution of complex engineering problems
- **PO2**. **Problem analysis**: Identify, formulate, review research literature, and analyze complex engineering problems reaching substantiated conclusions using first principles of mathematics, natural sciences, and engineering sciences.
- **PO3.** Design/development of solutions: Design solutions for complex engineering problems and design system components or processes that meet the specified needs with appropriate consideration for the public health and safety, and the cultural, societal, and environmental considerations.
- **PO4**. **Conduct investigations of complex problems**: Use research-based knowledge and research methods including design of experiments, analysis and interpretation of data, and synthesis of the information to provide valid conclusions.
- **PO5. Modern tool usage**: Create, select, and apply appropriate techniques, resources, and modern engineering and IT tools including prediction and modeling to complex engineering activities with an understanding of the limitations.
- **PO6.** The engineer and society: Apply reasoning informed by the contextual knowledge to assess societal, health, safety, legal and cultural issues and the consequent responsibilities relevant to the professional engineering practice.
- **PO7. Environment and sustainability**: Understand the impact of the professional engineering solutions in societal and environmental contexts, and demonstrate the knowledge of, and need for sustainable development.
- **PO8. Ethics**: Apply ethical principles and commit to professional ethics and responsibilities and norms of the engineering practice.
- **PO9.** Individual and team work: Function effectively as an individual, and as a member or leader in diverse teams, and in multidisciplinary settings.



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PO10. Communication: Communicate effectively on complex engineering activities with the engineering community and with society at large, such as, being able to comprehend and write effective reports and design documentation, make effective presentations, and give and receive clear instructions.

PO11. Project management and finance: Demonstrate knowledge and understanding of the engineering and management principles and apply these to one's own work, as a member and leader in a team, to manage projects and in multidisciplinary environments.

PO12. Life-long learning: Recognize the need for, and have the preparation and ability to engage in independent and life-long learning in the broadest context of technological change.

List of PEOs

PEO1. To produce graduates with understanding of fundamentals and applications of Electronics and Communication Engineering.

PEO2. To hone graduates with ability to apply, analyse, design and develop electronic systems.

PEO3. To enhance graduates with latest technologies to enable them to engineer products for real world problems.

PEO4. To build leadership qualities, management skills, communication skills, moral values & team spirit.

PEO5. Involve in lifelong self-learning, career enhancement and adapt to changing multidisciplinary professional and social needs.



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List of PSOs

PSO 1. Should be able to clearly understand the concepts and applications in the field of Communication/networking, signal processing, embedded systems and semiconductor technology.

PSO 2. Should be able to associate the learning from the courses related to Microelectronics, Signal processing, Microcomputers, Embedded and Communication Systems to arrive at solutions to real world problems.

PSO 3. Able to apply the concepts of Electronics and Communications to design the products in the field of VLSI, Embedded systems, Networking and Automation.

PSO 4. Demonstrate and implement variety of automation system by controlling, processing different signals according to the required specifications keeping in mind it's societal and environment effect.

PSO 5. An ability to make use of acquired technical knowledge to get employed in the field of Electronics and Communication.

Course Objectives and Outcomes (COs)

Course Name: COMPLEX VARIABLES AND TRANSFORMS

Course Objectives:

This course aims at providing the student to acquire the knowledge on the calculus of functions of complex variables. The student develops the idea of using continuous/discrete transforms.

Course Outcomes (CO): Student will be able to

- Understand the analyticity of complex functions and conformal mappings.
- Apply cauchy's integral formula and cauchy's integral theorem to evaluate improper integrals along contours.
- Understand the usage of laplace transforms, fourier transforms and z transforms.
- Evaluate the fourier series expansion of periodic functions.



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• Understand the use of fourier transforms and apply z transforms to solve difference equations.

Course Name: SIGNALS AND SYSTEMS

Course Objectives:

To introduce students to the basic idea of signal and system analysis and its characterization

in time and frequency domains.

To present Fourier tools through the analogy between vectors and signals.

To teach concept of sampling and reconstruction of signals.

To analyze characteristics of linear systems in time and frequency domains.

To understand Laplace and z-transforms as mathematical tool to analyze continuous and

discrete-time signals and systems.

Course Outcomes (CO):

Understand the mathematical description and representation of continuous-time and

discrete-time signals and systems. Also understand the concepts of various transform

techniques.

Apply sampling theorem to convert continuous-time signals to discrete-time signals and

reconstruct back, different transform techniques to solve signals and system related

problems.

Analyze the frequency spectra of various continuous-time and discrete-time signals using

different transform methods.

Classify the systems based on their properties and determine the response of them.

Course Name: ELECTRICAL ENGINEERING

Course Objectives:

Distinguish between classical method and Laplace transform approach in analyzing

• transient phenomenon in DC excitations

Understand and design the different types of filters.

To know about various characteristics of DC Generators and motors.

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- To know about principle of operation of a DC machine working as a generator and motor.
- To understand computation and predetermination of regulation of a 1- ϕ transformer.
- To know about principle of operation of three phase induction motor.

Course Outcomes (COs):

- Able to acquire knowledge about how to determine the transient response of R-L, R-C,
 R-L-C series circuits for D.C and A.C excitations.
- Able to solve the problems on R L C circuits for different excitations using different approaches.
- Analyze the complex circuits of R L C circuits.
- Able to solve the problems the e.m.f. generated on DC Generator
- Able to acquire knowledge about how to determine the efficiency and regulation of single-phase transformer and synchronous machine. Complex variables and Transforms.

Course Name: ANALOG CIRCUITS

Course Objectives:

- To review analysis & design of single stage amplifiers using BJT & MOSFETs at low and high frequencies.
- To understand the characteristics of Differential amplifiers, feedback and power amplifiers.
- To examine the response of tuned amplifiers and multivibrators
- To categorize different oscillator circuits based on the application
- To design the electronic circuits for the given specifications and for a given application.

Course Outcomes (CO):

 Understand the characteristics of differential amplifiers, feedback and power amplifiers. (L2)



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- Examine the frequency response of multistage and differential amplifier circuits using BJT & MOSFETs at low and high frequencies. (L3)
- Investigate different feedback and power amplifier circuits based on the application.
 (L4)
- Derive the expressions for frequency of oscillation and condition for oscillation of RC and LC oscillator circuits. (L4)
- Evaluate the performance of different tuned amplifiers and multivibrators (L5)
- Design analog circuits for the given specifications and application. (L6)

Course Name: MANAGERIAL ECONOMICS AND FINANCIAL ANALYSIS

Course Objectives:

- To inculcate the basic knowledge of micro economics and financial accounting
- To make the students learn how demand is estimated for different products, inputoutput
- relationship for optimizing production and cost
- To Know the Various types of market structure and pricing methods and strategy
- To give an overview on investment appraisal methods to promote the students to learn how to plan long-term investment decisions.
- To provide fundamental skills on accounting and to explain the process of preparing financial statements

Course Outcomes (CO):

- Define the concepts related to Managerial Economics, financial accounting and management.
- Understand the fundamentals of Economics viz., Demand, Production, cost, revenue and markets
- Apply the Concept of Production cost and revenues for effective Business decision
- Analyze how to invest their capital and maximize returns
- Evaluate the capital budgeting techniques



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 Develop the accounting statements and evaluate the financial performance of business entity.

Course Name: SIMULATION LAB

Course Objectives:

- To realize the concepts studied in theory
- To simulate various Signals and Systems through MATLAB
- To apply the concepts of signals to determine their energy, power, psd etc.
- To analyze the output of a system when it is excited by different types of deterministic and random signals.
- To generate random signals for the given specifications

Course Outcomes (CO):

- Learn how to use the MATLAB software and know syntax of MATLAB programming.
- Understand how to simulate different types of signals and system response.
- Find the Fourier Transform of a given signal and plot amplitude and phase characteristics.
- Analyze the response of different systems when they are excited by different signals and plot power spectral density of signals.
- Generate/Simulate different random signals for the given specifications

Course Name: ELECTRICAL ENGINEERING LAB

Course Objectives:

- Understand and experimentally verify various resonance circuits
- Apply and experimentally analyze two port network parameters
- To do experiments on DC Machines
- To do experiments on AC Machines

Course Outcomes (CO):

• To determine the various parameters experimentally



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- To understand various characteristics of DC generators and DC motors
- To predetermine the efficiency and regulation of a 1-φ transformer.

Course Name: ANALOG CIRCUITS LAB

Course Objectives:

- To review analysis & design of single stage amplifiers using BJT & MOSFETs at low and
- high frequencies.
- To understand the characteristics of Differential amplifiers, feedback and power
- amplifiers.
- To examine the response of tuned amplifiers and multivibrators
- To categorize different oscillator circuits based on the application
- To design the electronic circuits for the given specifications and for a given application.

Course Outcomes (CO):

- Know about the usage of equipment/components/software tools used to conduct the experiments in analog circuits.
- Conduct the experiment based on the knowledge acquired in the theory about various analog circuits using BJT/MOSFETs to find the important parameters of the circuit (viz.
 Voltage gain, Current gain, bandwidth, input and output impedances etc) experimentally.
- Analyze the given analog circuit to find required important metrics of it theoretically.
- Draw the relevant graphs between important metrics of the system from the observed measurements.
- Compare the experimental results with that of theoretical ones and infer the conclusions.
- Design the circuit for the given specifications.



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Course Name: Application Development with Python

Course Objectives:

- To learn the basic concepts of software engineering and life cycle models
- To explore the importance of Databases in application Development
- Acquire programming skills in core Python
- To understand the importance of Object-oriented Programming

Course Outcomes (CO):

Students should be able to

- Identify the issues in software requirements specification and enable to write SRS documents
- for software development problems
- Explore the use of Object-oriented concepts to solve Real-life problems
- Design database for any real-world problem
- Solve mathematical problems using Python programming language

Course Name: UNIVERSAL HUMAN VALUES

Course Objectives:

The objective of the course is fourfold:

- Development of a holistic perspective based on self-exploration about themselves (human being), family, society and nature/existence.
- Understanding (or developing clarity) of the harmony in the human being, family, ociety and nature/existence
- Strengthening of self-reflection.
- Development of commitment and courage to act.

Course Outcomes (CO):

By the end of the course,

• Students are expected to become more aware of themselves, and their surroundings (family, society, nature)



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- They would become more responsible in life, and in handling problems with sustainable solutions, while keeping human relationships and human nature in mind.
- They would have better critical ability.
- They would also become sensitive to their commitment towards what they have understood (human values, human relationship and human society).
- It is hoped that they would be able to apply what they have learnt to their own self in different day-to-day settings in real life, at least a beginning would be made in this direction.

COURSE NAME: PROBABILITY THEORY AND STOCHASTIC PROCESS

Course Objectives:

- To gain the knowledge of the basic probability concepts and acquire skills in handling
- situations involving more than one random variable and functions of random variables.
- To understand the principles of random signals and random processes.
- To be acquainted with systems involving random signals.
- To gain knowledge of standard distributions that can describe real life phenomena

Course Outcomes (CO):

- Understanding the concepts of Probability, Random Variables, Random Processes and their characteristics learn how to deal with multiple random variables, conditional probability, joint distribution and statistical independence. (L1)
- Formulate and solve the engineering problems involving random variables and random processes. (L2)
- Analyze various probability density functions of random variables. (L3)
- Derive the response of linear system for Gaussian noise and random signals as inputs. (L3)

COURSE NAME: DIGITAL LOGIC DESIGN

- To familiarize with the concepts of different number systems and Boolean algebra.
- To introduce the design techniques of combinational, sequential logic circuits.
- To model combinational and sequential circuits using HDLs.



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Course Outcomes (CO):

- Understand the properties of Boolean algebra, other logic operations, and minimization of
- Boolean functions using Karnaugh map.
- Make use of the concepts to solve the problems related to the logic circuits.
- Analyze the combinational and sequential logic circuits.
- Develop digital circuits using HDL, and Compare various Programmable logic devices
- Design various logic circuits using Boolean algebra, combinational and sequential logic circuits.

COURSE NAME: ELECTROMAGNETIC WAVES AND TRANSMISSION LINES

Course Objectives:

- To introduce fundamentals of static and time varying electromagnetic fields.
- To teach problem solving in Electromagnetic fields using vector calculus.
- To demonstrate wave concept with the help of Maxwell's equations.
- To introduce concepts of polarization and fundamental theory of electromagnetic waves in transmission lines and their practical applications.
- To analyze reflection and refraction of electromagnetic waves propagated in normal and oblique incidences.

Course Outcomes (CO):

- Explain basic laws of electromagnetic fields and know the wave concept. (L2)
- Solve problems related to electromagnetic fields. (L3)
- Analyze electric and magnetic fields at the interface of different media. (L3)
- Derive Maxwell's equations for static and time varying fields. (L3)
- Analogy between electric and magnetic fields. (L5)
- Describes the transmission lines with equivalent circuit and explain their characteristic with various lengths.



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COURSE NAME: COMMUNICATION SYSTEMS

Course Objectives:

- To introduce various modulation and demodulation techniques of analog and digital
- communication systems.
- To analyze different parameters of analog and digital communication techniques.
- To Know Noise Figure in AM & FM receiver systems.
- To understand Function of various stages of AM, FM transmitters and Know Characteristics of
- AM &FM receivers.
- To analyze the performance of various digital modulation techniques in the presence of AWGN.
- To evaluate the performance of each modulation scheme to know the merits and demerits interms of bandwidth and power efficiency

Course Outcomes (CO):

- Recognize/List the basic terminology used in analog and digital communication techniques for transmission of information/data.
- Explain/Discuss the basic operation of different analog and digital communication systems at baseband and passband level.
- Compute various parameters of baseband and passband transmission schemes by applying basic engineering knowledge.
- Analyze/Investigate the performance of different modulation & demodulation techniques to solve complex problems in the presence of noise.
- Evaluate/Assess the performance of all analog and digital modulation techniques to know the merits and demerits of each one of them in terms of bandwidth and power efficiency.

COURSE NAME: LINEAR AND DIGITAL IC APPLICATIONS

- To introduce the basic building blocks of linear integrated circuits.
- To teach the linear and non-linear applications of operational amplifiers.
- To introduce the theory and applications of PLL.
- To introduce the concepts of waveform generation and introduce some special function ICs.
- Exposure to digital IC's



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Course Outcomes (CO):

- List out the characteristics of Linear and Digital ICs.
- Discuss the various applications of linear & Digital ICs.
- Solve the application-based problems related to linear and digital ICs.
- Analyze various applications-based circuits of linear and digital ICs.
- Design the circuits using either linear ICs or Digital ICs from the given specifications.

COURSE NAME: DIGITAL LOGIC DESIGN LAB

Course Objectives:

- To understand various pin configurations of the Digital ICs used in the laboratory
- To conduct the experiments and verify the truth tables of various logic circuits.
- To analyze the logic circuits
- To design sequential and combinational logic circuits and verify their properties.
- To design of any sequential/combinational circuit using Hardware Description Language.

Course Outcomes (CO):

- Understand the pin configuration of various digital ICs used in the lab
- Conduct the experiment and verify the properties of various logic circuits.
- Analyze the sequential and combinational circuits.
- Design of any sequential/combinational circuit using Hardware/ HDL.

COURSE NAME: COMMUNICATION SYSTEMS LAB

- To understandthe basics of analog and digital modulation techniques.
- To Integrate theory with experiments so that the students appreciate the knowledge gained from the theory course.
- To design and implement different modulation and demodulation techniques and their applications.
- To develop cognitive and behavioral skills for performance analysis of various modulation techniques.



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Course Outcomes (CO):

- Know about the usage of equipment/components/software tools used to conduct the experiments in analog and digital modulation techniques.
- Conduct the experiment based on the knowledge acquired in the theory about modulation and
- demodulation schemes to find the important metrics of the communication system experimentally.
- Analyze the performance of a given modulation scheme to find the important metrics of the system theoretically.
- Draw the relevant graphs between important metrics of the system from the observed measurements.
- Compare the experimental results with that of theoretical ones and infer the conclusions.

COURSE NAME: LINEAR AND DIGITAL IC APPLICATIONS LAB

Course Objectives:

The objective of the course is to learn design, testing and characterizing of circuit behaviour with digital and analog ICs.

Course Outcomes (CO):

- Understand the pin configuration of each linear/ digital IC and its functional diagram.
- Conduct the experiment and obtain the expected results.
- Analyze the given circuit/designed circuit and verify the practical observations with the analyzed results.
- Design the circuits for the given specifications using linear and digital ICs.
- Acquaintance with lab equipment about the operation and its use.

COURSE NAME: SOFT SKILLS

- To encourage all round development of the students by focusing on soft skills
- To make the students aware of critical thinking and problem-solving skills
- To develop leadership skills and organizational skills through group activities
- To function effectively with heterogeneous teams



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Course Outcomes (CO):

By the end of the program students should be able to

- Memorize various elements of effective communicative skills
- Interpret people at the emotional level through emotional intelligence
- apply critical thinking skills in problem solving
- analyse the needs of an organization for team building
- Judge the situation and take necessary decisions as a leader
- Develop social and work-life skills as well as personal and emotional well-being.

COURSE NAME: DESIGN THINKING FOR INNOVATION

Course Objectives:

The objective of this course is to familiarize students with design thinking process as a tool for breakthrough innovation. It aims to equip students with design thinking skills and ignite the minds to create innovative ideas, develop solutions for real-time problems.

Course Outcomes (CO):

- Define the concepts related to design thinking.
- Explain the fundamentals of Design Thinking and innovation
- Apply the design thinking techniques for solving problems in various sectors.
- Analyse to work in a multidisciplinary environment
- Evaluate the value of creativity
- Formulate specific problem statements of real time issues

COURSE NAME: CONTROL SYSTEMS ENGINEERING

- To introduce concepts of open loop and closed loop systems, mathematical models of mechanical and electrical systems and concept of feedback.
- To describe characteristics of the given system in terms of the transfer function.
- To provide knowledge in analyzing the system response in time-domain and frequency domain
- To impart skills for designing different control systems for different applications as per given specifications.



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• To introduce concepts of state variable analysis and design.

Course Outcomes:

- Identify open and closed loop control system
- Formulate mathematical model for physical systems
- Use standard test signals to identify performance characteristics of first and second-order
- Systems
- Analyze stability of the closed and open loop systems
- Design closed-loop control system to satisfy dynamic performance specifications using frequency response, root-locus, and state-space techniques.

COURSE NAME: DIGITAL SIGNAL PROCESSING

Course Objectives:

- To describe discrete time signals and systems.
- To teach importance of FFT algorithm for computation of Discrete Fourier Transform.
- To expose various implementations of digital filter structures.
- To present FIR and IIR Filter design procedures.
- To outline need of Multi-rate Processing.

Course Outcomes:

- Formulate difference equations for the given discrete time systems
- Apply FFT algorithms for determining the DFT of a given signal
- Compare FIR and IIR filter structures
- Design digital filter (FIR & IIR) from the given specifications
- Outline the concept of multirate DSP and applications of DSP.



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COURSE NAME: MICROPROCESSORS AND MICROCONTROLLERS

Course Objectives:

- To introduce fundamental architectural concepts of microprocessors and microcontrollers.
- To impart knowledge on addressing modes and instruction set of 8086 and 8051
- To introduce assembly language programming concepts
- To explain memory and I/O interfacing with 8086 and 8051
- To introduce 16 bit and 32-bit microcontrollers.

Course Outcomes:

- Distinguish between microprocessors & microcontrollers
- Develop assembly language programming
- Describe interfacing of 8086 with peripheral devices
- Design applications using microcontrollers

COURSE NAME: COMPUTER ARCHITECTURE & ORGANIZATION

Course Objectives:

The purpose of the course is to introduce principles of computer organization and the basic architectural concepts.

Course Outcomes:

- Understand the basics of instructions sets and their impact on processor design.
- Demonstrate an understanding of the design of the functional units of a digital computer system.
- Evaluate cost performance and design trade-offs in designing and constructing a computer processor including memory.
- Design a pipeline for consistent execution of instructions with minimum hazards.
- Recognize and manipulate representations of numbers stored in digital computers.



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COURSE NAME: DIGITAL SIGNAL PROCESSING LAB

Course Outcomes:

- Implement various DSP Algorithms using software packages.
- Implement DSP algorithms with Digital Signal Processor.
- Analyze and observe magnitude and phase characteristics (Frequency response
- Characteristics) of digital IIR-Butterworth, Chebyshev filters.
- Analyze and observe magnitude and phase characteristics (Frequency response
- Characteristics) of digital FIR filters using window techniques.
- Analyze digital filters using Software Tools.

COURSE NAME: MICROPROCESSORS AND MICROCONTROLLERS LAB

Course Objectives:

To acquire the knowledge on microprocessors and microcontrollers, interfacing various peripherals, configure and develop programs to interface peripherals/sensors.

Course Outcomes:

- Formulate problems and implement algorithms using Assembly language.
- Develop programs for different applications.
- Interface peripheral devices with 8086 and 8051.
- Use Assembly/Embedded C programming approach for solving real world problems.

COURSE NAME: ANTENNAS&MICROWAVE ENGINEERING

- To enable the student to understand the basic principles in antenna and microwave system design
- To make the student to acquire knowledge in the area of various antenna designs.
- To enhance the student knowledge in the area of microwave components and antenna for
- practical applications.



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Course Outcomes:

At the end of this course, the students will be able to

- Learn about the antenna's basics and wire antennas.
- Gain knowledge on few types of antennas, their operation and applications.
- Understand the uses of antenna arrays and analyze waveguides and resonators
- Analyze various microwave components and understand the principles of different microwave sources.
- Gain knowledge on microwave semiconductor devices and microwave measurements.

COURSE NAME: VLSI DESIGN

Course Objectives:

- To give exposure to different steps involved in fabrication of ICs using MOS transistor,
- CMOS/BICOM transistors and passive components.
- To provide knowledge on electrical properties of MOS &BICMOS devices to analyze the behavior of inverters designed with various loads.
- To provide concepts to design building blocks of data path of any system using gates.
- To teach about basic programmable logic devices and testing of CMOS circuits.

Course Outcomes:

- Acquire qualitative knowledge about the fabrication process of integrated circuit using MOS transistors,
- Draw the layout of any logic circuit which helps to understand and estimate parasitic of any logic circuit
- Design building blocks of data path using gates.
- Design simple memories using MOS transistors and can understand design of large memories
- Understand the concept of testing and adding extra hardware to improve testability of system

COURSE NAME: DATA COMMUNICATION & NETWORKS

Course Objectives:

To provide a solid conceptual understanding of the fundamentals of data communications and computer networks.



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Course Outcomes:

- Understand the basics of data communication, networking, internet and their importance.
- Analyze the services and features of various protocol layers in data networks.
- Differentiate wired and wireless computer networks
- Analyse TCP/IP and their protocols.
- Recognize the different internet devices and their functions.

COURSE NAME: ELECTRONIC MEASUREMENTS AND INSTRUMENTATION

Course Objectives:

The objective of the course is to introduce the fundamentals of Electronics Instruments and Measurement providing an in-depth understanding of Measurement errors, Bridge measurements, Digital Storage Oscilloscope, Function Generator and Analyzer, Display devices, Data acquisition systems and transducers.

Course Outcomes:

- Explain operation of various instruments required in measurements
- Apply measurement techniques for different types of tests
- Select specific instruments for specific measurement function
- Use oscilloscope to determine frequency and phase of a sinusoidal signal
- Compare different types of bridge circuits
- Analyze various measuring techniques for both electrical and nonelectrical quantities

COURSE NAME: EMBEDDED SYSTEM DESIGN

- To teach the basics of an embedded system and RTOS.
- To introduce the typical components of an embedded system & different communication interfaces.
- To provide knowledge on the design process of embedded system applications



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Course Outcomes:

- Identify hardware and software components of an embedded system
- Learn the basics of OS and RTOS
- Illustrate different Inter Process Communication (IPC) mechanisms used by
- tasks/process/tasks to communicate in multitasking environment
- Design simple embedded system-based applications

COURSE NAME: OPTICAL COMMUNICATIONS

Course Objectives:

- To understand the construction and characteristics of optical fibre cable.
- To develop the knowledge of optical signal sources and power launching.
- To identify and understand the operation of various optical detectors.
- To understand the design of optical systems and WDM.

Course Outcomes:

- At the end of the course, the student will be able to:
- Understand and analyze the constructional parameters of optical fibres.
- Estimate the losses due to attenuation, absorption, scattering and bending.
- Compare various optical detectors and choose suitable one for different applications.

COURSE NAME: ANTENNAS & MICROWAVE ENGINEERING LAB

Course Objectives:

- To understand the working, different microwave components and verify characteristics using
- microwave bench setup.
- To study various antennas

Course Outcomes:

- At the end of this course, the students will be able to
- Understand the working, different microwave components and sources in a microwave bench
- Verify the characteristics of various microwave components using microwave bench setup



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- Design and study of various antennas
- Analyze performance characteristics of Antennas

COURSE NAME: VLSI DESIGN LAB

Course Outcomes:

- Design any logic circuit using CMOS transistor.
- Use different software tools for analysis of circuits.
- Design layouts to the CMOS circuits.
- Use different software tools for analog layout

COURSE NAME: DSP PROCESSORS & ARCHITECTURES

Course Objectives:

- To describe unique features of Digital signal processing.
- To demonstrate various computational parameters of DSP devices.
- To introduce architectural improvements in programmable DSP devices.
- To expose to basic DSP algorithms.
- To outline DSP processors for developing various applications.

Course Outcomes:

- Summarize features of Digital Signal Processing
- Evaluate dynamic ranges and precision for the given DSP system
- Explain architectural features of DSP processors
- Analyze performance of DSP algorithms on programmable DSP platform for given application
- Select DSP processors for building real time applications

COURSE NAME: INTRODUCTION TO INTERNET OF THINGS

Course Objectives:

Students will understand the concepts of Internet of Things and can able to build IoT applications.

Course Outcomes:

Understand the concepts of Internet of Things



DEPARTMENT OF ELECTRONICS AND COMMUNICATIONS ENGINEERING

- Identify hardware and software components of Internet of Things
- Analyze basic communication protocols
- Design IoT applications in different domain and be able to analyze their performance

COURSE NAME: SATELLITE COMMUNICATIONS

Course Objectives:

To introduce various aspects in the design of systems for satellite communication.

Course Outcomes:

- Learn the dynamics of the satellite.
- Understand the communication satellite design.
- Understand how analog and digital technologies are used for satellite communication networks.
- Learn the design of satellite links.
- Study the design of Earth station and tracking of the satellites.

COURSE NAME: DIGITAL IMAGE PROCESSING

Course Objectives:

This course is designed to enable the students to familiarize themselves with basic concepts of digital image processing and different image transforms and learn various image processing techniques like image enhancement, restoration, segmentation and compression

Course Outcomes:

After completion of the course, students will be able to

- Perform image manipulations and different digital image processing techniques
- Illustrate basic operations like Enhancement, segmentation, compression, Image
- transforms and restoration techniques on image.
- Analyze pseudo and full color image processing techniques.
- Apply various morphological operators on images



DEPARTMENT OF ELECTRONICS AND COMMUNICATIONS ENGINEERING

COURSE NAME: RADAR ENGINEERING

Course Objectives:

- To make student to acquire the knowledge on types of Radars, working principles, tracking a
- target, applications and understand on phased array antennas, navigational aids

Course Outcomes:

- Learn the basic working principle of Radar and target detection procedure
- Know the working and applications of CW and Frequency modulated Radar
- Gain the knowledge of about MTI and Pulse Doppler Radar
- Understand different methods of tracking a target and analyze the effect of noise at the receiver
- Learn about the phased array antennas and navigational aids

COURSE NAME: CELLUAR & MOBILE COMMUNICATIONS

Course Objectives:

- To explain cell coverage for signal and traffic, diversity techniques and mobile antennas by the use of Engineering Mathematics.
- To present impairments due to multipath fading channel, fundamental techniques to
- overcome different fading effects, frequency management, Channel assignment and types of handoffs.
- To teach concepts and solve problems on mobile antennas and cellular systems.

Course Outcomes:

- Know about cell coverage for signal and traffic, diversity techniques and mobile antennas by the use of Engineering Mathematics
- Explain impairments due to multipath fading channel, fundamental techniques to overcome different fading effects, frequency management, Channel assignment and types of handoff
- Apply concepts to solve problems on mobile antennas and cellular systems
- Analyze Co-channel and Non-Co-channel interferences, different Hand-offs and dropped call rates



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• Evaluate performance of dropped call rate and false alarm rate and compare different handoffs.



SULLURPET - 524121

ACADEMIC YEAR 2021 / 2022 EVEN SEMESTER U.G. PROJECT REVIEW FORM

SECTION 5: PROJECT FINAL REVIEW

1.	Final review comments:	Date:	02/03/3035
----	------------------------	-------	------------

Reviewers:

- · Reviewer 1 Mo. J. mahegwar Reddy
- · Reviewer 2 Dr. m. Chiran seev?
- · Reviewer 3 S. Rajesh
- · Reviewer 4 K. Subtamanyam

PO No.	Description	How the Outcome was achieved (Each in 10 words)
	Project completion as per plan at the beginning of the project	Prosect Completed asperplan
	a) Scope	
	b) Testing	
	c) Verification / validation	
	d) Implementation	
PO 1	Engineering Knowledge	
PO 2	Problem Analysis	V
PO 3	Design/development of solutions	
PO 4	Conduct investigations of complex problems	
PO 5	Modern tool usage	
PO 6	The engineer and society	
PO 7	Environment and sustainability	
PO 8	Ethics	
PO 9	Individual and team work	V
PO 10	Communication	
PO 11	Project management and finance	
PO 12	Life-long learning	
	Applying for patent	
	Applying for research proposal	
	Product development and IPR	
	Start up initiation	

General Remarks

Project implement on time as well as in real time, students involved a lot and come up with a socioeconomy polution

Project Guide's Signature:

HOD's Signature: 4. Cliff.

Date: 02/07/2022

Date: 02/07/0020

Page 5 of 5



SULLURPET - 524121

ACADEMIC YEAR 2021 / 2022 EVEN SEMESTER U.G. PROJECT REVIEW FORM

SECTION 5: PROJECT FINAL REVIEW

1.	Final review comments:	Date:	08/7/20

Reviewers:

Reviewer 1 MY · J. Mahasum Roddy
Reviewer 2 M DY · M · Chironylean
Reviewer 3 MY · S. Rojesh
Reviewer 4 mg / S. Rojesh

· Reviewer 4 my · K · Subnamonyam

PO No.	Description Description	How the Outcome was achieved (Each in 10 words)	
	Project completion as per plan at the beginning of the project	project compreted as expected	
	a) Scope		
	b) Testing		
	c) Verification / validation		
	d) Implementation		
PO 1	Engineering Knowledge	√	
PO 2	Problem Analysis	V	
PO 3	Design/development of solutions		
PO 4	Conduct investigations of complex problems		
PO 5	Modern tool usage		
PO 6	The engineer and society		
PO 7	Environment and sustainability		
PO 8	Ethics		
PO 9	Individual and team work	V	
PO 10	Communication		
PO 11	Project management and finance		
PO 12			
	Applying for patent		
	Applying for research proposal		
	Product development and IPR		
	Start up initiation		

General Remarks

Ay the Students involved in the work and completed it

Project Guide's Signature:

HOD's Signature: 4. Chi

Date: 2/7/2012



SULLURPET - 524121

ACADEMIC YEAR 2021 / 2022 EVEN SEMESTER U.G. PROJECT REVIEW FORM

SECTION 5: PROJECT FINAL REVIEW

1.	Final review comments:	Date:	02	70	2022
 .1					

Reviewers:

- · Reviewer 1 Mr. J. Mahes war Reddy
- · Reviewer 2 Dr. M. Chivanjeevi

· Reviewer 3 S. Rajesh

PO No.	Description	How the Outcome was achieved (Each in 10 words)	
	Project completion as per plan at the	project completed as per plan	
	beginning of the project		
	a) Scope		
	b) Testing		
	c) Verification / validation	7	
	d) Implementation	4	
PO 1	Engineering Knowledge	V	
PO 2	Problem Analysis	V	
PO 3	Design/development of solutions		
PO 4	Conduct investigations of complex		
	problems		
PO 5	Modern tool usage		
PO 6	The engineer and society		
PO 7	Environment and sustainability		
PO 8	Ethics	1	
PO 9	Individual and team work	V	
PO 10	Communication		
PO 11	Project management and finance		
PO 12	Life-long learning		
	Applying for patent		
	Applying for research proposal		
	Product development and IPR		
	Start up initiation		

General Remarks

All Students to Envolved in project and completed Project successfully.

M. Winyarani Project Guide's Signature:

HOD's Signature: 4. Chi

Date: 02/07/2022

Date: 02/07/2022

Page 5 of 5



SULLURPET - 524121

ACADEMIC YEAR 2021 / 2022 EVEN SEMESTER U.G. PROJECT REVIEW FORM

SECTION 5: PROJECT FINAL REVIEW

1.	Final review comments:	Date:	02-07-2022

Reviewers:

Reviewer 1 Mr. J. Mahistian Reddy

Reviewer 2 Dr. M. Chiranjeevi

Reviewer 3 Mr. S. Rajesh

Reviewer 4 Mr. K. Subramanyam

PO No.	Description	How the Outcome was achieved
		(Each in 10 words)
	Project completion as per plan at the	Project Completed as per planning
	beginning of the project	
	a) Scope	
	b) Testing	
	c) Verification / validation	
	d) Implementation	
PO 1	Engineering Knowledge	
PO 2	Problem Analysis	✓
PO 3	Design/development of solutions	
PO 4	Conduct investigations of complex	
	problems	
PO 5	Modern tool usage	,
PO 6	The engineer and society	✓
PO 7	Environment and sustainability	
PO 8	Ethics	
PO 9	Individual and team work	<u> </u>
PO 10	Communication	
PO 11	Project management and finance	
PO 12	Life-long learning	
	Applying for patent	
	Applying for research proposal	
	Product development and IPR	
	Start up initiation	

General Remarks

All the Students shown their 100% effect in Completion of the Project.

Project Guide's Signature:

HOD's Signature: M. Cluy

Date: 02/07/2K22

Date: 2/7/2022

Page 5 of 5

Department of Electrical and Electronics Engineering

Result Analysis

BATCH:2021-22

DAI	CI1.2021-22		T .	
1	18F81A0201	Jahnavi . U	~	
2	18F81A0202	Jayanth babu . V		×
3	18F81A0203	jyotheesh . K		×
4	18F81A0204	Sathish . K		×
5	19F85A0202	Chaitanya Kumar . A		×
6	19F85A0203	Dinesh. G		×
7	19F85A0207	Kiran Reddy. P	~	
8	19F85A0209	Pavan Kumar. I		×
9	19F85A0210	Shahid Afridh. SD		×
10	19F85A0211	Sumanth Kumar. K		×
11	19F85A0213	Venkata Ramana Reddy.G		×
12	19F85A0214	Venkatesh. K		×
13	19F85A0215	Venkatesh. S		×
14	19F85A0216	Vinay. T		×
15	19F85A0217	Vinay Kumar. T	1	×
16	19F85A0218	Vinutha.P	V	
17	19F85A0221	Yuvaraja.B		×

BATCH:2020-21

11.2020 21			
17F81A0201	Anupa. N		
17F81A0202	Chandrika. Y		
17F81A0203	Chandu. D	~	
17F81A0204	Hema. D		×
17F81A0205	Maneesha.b		
17F81A0206	Mani sekhar. V		
17F81A0207	Mounika. S		
17F81A0209	Pragathi. M	~	
17F81A0210	Saigeetha. A		×
	Varalakshmi. K	~	
17F81A0212	Varamma. K	~	
17F81A0213	Venkata lakshmi. G		×
17F81A0214			×
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	17F81A0201 17F81A0202 17F81A0203 17F81A0204 17F81A0205 17F81A0206 17F81A0207 17F81A0209 17F81A0210 17F81A0211 17F81A0212	17F81A0201 Anupa. N 17F81A0202 Chandrika. Y 17F81A0203 Chandu. D 17F81A0204 Hema. D 17F81A0205 Maneesha.b 17F81A0207 Mounika. S 17F81A0209 Pragathi. M 17F81A0210 Saigeetha. A 17F81A0211 Varalakshmi. K 17F81A0212 Varamma. K 17F81A0213 Venkata lakshmi. G 17F81A0214 Vinay Kumar. A 18F85A0201 Anil.Y 18F85A0202 Mahesh .D 18F85A0204 Manasa.A 18F85A0205 Poornima.S 18F85A0207 Rammohan.R 18F85A0210 Siva Prasad .K 18F85A0211 Siva Sai.E	17F81A0201 Anupa. N 17F81A0202 Chandrika. Y 17F81A0203 Chandu. D 17F81A0204 Hema. D 17F81A0205 Maneesha.b 17F81A0206 Mani sekhar. V 17F81A0207 Mounika. S 17F81A0209 Pragathi. M 17F81A0210 Saigeetha. A 17F81A0211 Varalakshmi. K 17F81A0212 Varamma. K 17F81A0213 Venkata lakshmi. G 17F81A0214 Vinay Kumar. A 18F85A0201 Anil. Y 18F85A0202 Mahesh .D 18F85A0203 Manasa.A 18F85A0204 Manasa.Y 18F85A0205 Poornima.S 18F85A0209 Sathish.T 18F85A0210 Siva Prasad .K 18F85A0211 Siva Sai.E

24	18F85A0214	Vinay.E		×
25	18F85A0215	Yaswanth.P	~	
26	18F85A0216	Yuga Sai.CH		X

BATCH:2019-20

2	16F81A0202	Naseema Bhanu. P		X
3		Charan sai .B	~	
4		Geetha prasd .S		X
5		Muni sai Teja .M	~	
6	17F85A0204	Prabhudev . M	~	
7	17F85A0205	Subramanyam.G	~	
8	17F85A0206	Surendra.G		X
9	17F85A0208	Venkatesh .CH		X

BATCH:2018-19

	Character - Charac	
	NO ADMISSION	1 1
1	NO ADMISSION	

BATCH:2017-18

S.No	Roll Number	Name of the Student	PASS	FAIL
1	14F81A0201	Akhil. M	~	
2	14F81A0202	Dilli Babu. K		×
3	14F81A0203	Hemavathy. K	~	
4	14F81A0205	Kavya. G	~	
5	14F81A0206	Lavanya. CH	~	
6	14F81A0208	Priyanka. K	~	
7	14F81A0209	Sai Greeshma. N		×
8	14F81A0210	Sai. M		×
9	14F81A0211	Sandhya. V	~	
10	14F81A0212	Sruthi. L	~	
11	14F81A0214	Vikesh. P		×
12	15F85A0201	Manasa.M	~	
13	13F81A0216	Siva Koti.K		×

Dept of Electrical & Electronics Engs Gokula Krishaa College of Engg, Sulluroot-524121, SPSR Nellore Dt. A.P. PRINCIPAL

Gokula Krishna College of Engineering
Behind R.T.C. Depot, Sullurpet
SPSR Nellore Dt, Andhra Pradesh - 524 121



(Affiliated to JNTUA Ananthapuramu and approved by AICTE, New Delhi)

Department of Electrical and Electronics Engineering

BATCH:2021-22



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR ANANTHAPURAMU - 515002(A.P) - INDIA

PC CMM Application Acknowledgement

Important Note: Please check carefully the spellings of Student Name, Father's Name, Mother's Name before uploading the same

> 22-07-2022 Date of Application

GKCE-SULLURPET College Name

HallTicket Number 19F85A0218 Student Name P VINUTHA 737350878566 Aadhaar Number

P BABU Father Name P KANCHANA Mother Name

NAIDUVINUTHA123@GMAIL.COM Email ID

Mobile Number 6281286023 Gender Female

Note: Student should get this acknowledgement attested by your college principal along with this application and submit the same copy to the university and collect the PC CMM 01-08-2022 onwards.

l g

Principal Dr. MUCHALALA SURESH, ACT.

Gokula Krishna Col

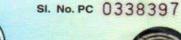
SPSR NeiloSeal & Signature

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR ANANTHAPURAMU - 515 002, ANDHRA PRADESH, INDIA

Adm.no. 18F85A0202

Aadhaar.no. 332456058351







PROVISIONAL CERTIFICATE

This is to certify that Mr. DHAMA MAHESH Son of Sri D HARI & Smt D ARUNA, Fassed the

ELECTRICAL & ELECTRONICS ENGINEERING

Examination of this University held in the month of July 2021 and that he was placed in

*** First Class with Distinction ***

He has satisfied all the requirements for the award of B. TECH Degree of the

Jawaharlal Nehru Technological University Unantapur, Ananthapuramu.

Medium of Instruction : English

Wednesday, 22 September 2021

DIRECTOR OF EVALUATION Anantapur - A.P.

REGISTRAR

Adm.no. 17F85A0201

Aadhaar.no. 742282098745



SI. No. PC 0309599





PROVISIONAL CERTIFICATE

This is to certify that Mr. BADDIGA CHARAN SAI Son of Sri BADDIGA SREENIVASULU & Smt BADDIGA ESWARAMMA, Fassed the

ELECTRICAL & ELECTRONICS ENGINEERING

Examination of this University held in the month of September 2020 and that he was placed in

*** First Class ***

He has satisfied all the requirements for the award of B. TECH Degree of the

Jawaharlal Nehru Technological University anantapur, Ananthapuramu.

Medium of Instruction: English

Wednesday, 28 October 2020

DIRECTOR OF EVALUATION

REGISTRAR

Note: All UG Final Semester examinations for the scademic year 2019-2020 were originally scheduled in the month of April 2020. But, in view of Covid-19 and as per UGC guidelines these examinations were conducted in the month of September 2020.

Adm.no. 14F81A0206

Aadhaar.no. 997842121551



SI. No. PC 0271681





PROVISIONAL CERTIFICATE

This is to certify that Ms. CHITTETI LAVANYA

Daughter of Sri CHITTETI SREENIVASULU & Smt CHITTETI SUPRIYA, Passed the

ELECTRICAL & ELECTRONICS ENGINEERING

Examination of this University held in the month of April 2018 and that she was placed in

*** First Class with Distinction ***

She has satisfied all the requirements for the award of B. TECH Degree of the Jawaharlal Nehru Technological University anantapus, Ananthapuramu.

Wednesday, 3 October 2018

DIRECTOR OF EVALUATION



GOKULA KRISHNA COLLEGE OF ENGINEERING: SULLURUPET
(Affiliated to JNTUA Ananthapuramu and approved by AICTE, New Delhi)
Department of Mechanical Engineering

BATCH: 2021-22

SI. NO.	ROLL NO	Name of the student	PASS	FAIL
1	18F81A0301	Balaji . B		X
2	18F81A0302	Bhanuprasad . G	√	
3	18F81A0303	Bharath . P		X
4	18F81A0304	Charankumar . P		X
5	18F81A0306	Hemaprasad . A	√	
6	18F81A0307	Harshavardhan . V		X
7	18F81A0308	Jeevan sai kumar . B		X
8	18F81A0309	Kiran . K		X
9	18F81A0310	Kotaiah . M		X
10	18F81A0311	Koteswara Rao . A	√	
11	18F81A0312	Lakshman narayana . M		X
12	18F81A0314	Munisekhar . V	,	X
13	18F81A0315	Pavan Kumar . M		X
14	18F81A0316	Nani . CH		X
15	18F81A0317	Revathi . M		x
16	18F81A0318	Surya . M		X
17	18F81A0319	Teja . A		X
18	18F81A0320	Thulasiram . T		X
19	18F81A0321	Upendra . A		X
20	18F81A0322	Venkatesh . J		X
21	16F81A0342	Vijaykumar. V		X
22	19F85A0301	Ajay Kumar.CH		X
23	19F85A0302	Bhanuprakash. B		X
24	19F85A0303	Bhanu Vikas. CH		x
25	19F85A0305	Ganesh Aravindaswamy.T		X
26	19F85A0306	Hemanth Kumar. CH	√	
27	19F85A0308	Muni Karthik. K		X
28	19F85A0309	Naresh, G		X
29	19F85A0310	Nithish Kumar.M	√	



(Affiliated to JNTUA Ananthapuramu and approved by AICTE, New Delhi)

Department of Mechanical Engineering

30	19F85A0311	Pavan Kalyan. P	X
31	19F85A0312	Sammya. A	x
32	19F85A0313	Srilekha. N	X
33	19F85A0314	Tharun Teja. G	X
34	19F85A0315	Venkateswarlu.G	X
35	19F85A0316	Venu. N	x
36	18F85A0302	Bala subramanyam. A	X
37	182U1A0318	Muneendra.I	X

HOD

PRINCIPAL

Gokula Krishna College of Engineering Behind R.T.C. Depot, Sullurpet SPSR Nellore Dt, Andhra Pradesh - 524 121

BATCH: 2020-21

S.NO	ROLL NO	NAME OF THE STUDENT	PASS	FAIL
1	17F81A0301	Ajith. K		X
2	17F81A0302	Akash. N		X
3	17F81A0303	Anand kumar. P	√	
4	17F81A0304	Balaji. K	√	
5	17F81A0305	Bhanu Prakash. M		
6	17F81A0306	Chandu. T	√	
7	17F81A0307	Chengaiah. T		X
8	17F81A0308	Ganesh.v		X
9	17F81A0309	Jamaludheen. SK		X
10	17F81A0310	Karthik. A		X
11	17F81A0311	Krishna Vamsi. M		X
12	17F81A0312	Laluprasad yadav. M	√	
13	17F81A0313	Mahesh babu. E		X
14	17F81A0314	Murali. T		X
15	17F81A0315	Nagabhushanam. Ch		X
16	17F81A0316	Nithin. Y	/	
17	17F81A0318	Rakesh. N	√	
18	17F81A0319	Rakesh. Y	√	
19	17F81A0320	Saiteja. D		X
20	17F81A0321	Sandeep. K		X
21	17F81A0322	Sandhish. R	√	
22	17F81A0326	Sumanth. G	√	
23	17F81A0327	Teja. K		X
24	17F81A0328	Uday kumar. M	√	
25	17F81A0329	Vamsi. S		X
26	17F81A0330	Venkatesh. B	√	
27	17F81A0331	Vijay Kumar. M	√	
28	17F81A0332	Viswa. G		X
29	17F81A0333	Yeswanth. A		X
30	18F85A0303	Bhanu Prakash.P	√	
31	18F85A0304	Dinesh .B	√	
32	18F85A0306	Harshavardhan .E	√	
33	18F85A0307	Harshini .K	√	
34	18F85A0308	Karthik.K	√	
35	18F85A0309	Kiran . T	√	

37	18F85A0312	Murali . G	√	
38	18F85A0314	Sai Ragani	11.	X
39	18F85A0315	Suresh . D	√	
40	18F85A0316	Uday Kumar. A		X
41	18F85A0317	Vamsi Krishana. N	√	
42	18F85A0318	Veera Kumar .P	√	
43	18F85A0319	Vinay Kumar . K	√	

HOD

PRINCIPAL

PRINCIPAL

Gokula Krishna College of Engineering
Behind R.T.C. Depot, Sullurpet

SPSR Nellore Dt, Andhra Pradesh - 524 121

BATCH: 2019-20

S.No.	Roll Number	Name of the Student	PASS	FAIL
1	16F81A0301	Abbas. D	√	
2	16F81A0302	Anilkumar. P	√	
3	16F81A0304	Charan Kumar. A		X
4	16F81A0306	Gowtham. N		X
5	16F81A0307	Gunasekhar. B		X
6	16F81A0308	Gunasekhar, K	√	
7	16F81A0309	Guravaiah. V		X
8	16F81A0311	Hemanth. G	√	
9	16F81A0312	Hemanth. T		X
10	16F81A0313	Janardhan. K	√	
11	16F81A0314	Koushik Kumar. P	√	
12	16F81A0315	Kumar. S		X
13	16F81A0318	Nagaraja. T	√	
14	16F81A0319	Netaji. S		X
15	16F81A0320	Pavan Kumar. K		X
16	16F81A0321	Pavankumar. CH	√	
17	16F81A0322	Prasad. K		X
18	16F81A0323	Prasanth. A	√	
19	16F81A0324	Praveen. N		X
20	16F81A0325	Prudhvi. M		X
21	16F81A0326	Ravi Teja. P		X
22	16F81A0327	Rohith kumar. K		X
23	16F81A0328	Saikiran. K		X
24	16F81A0329	Sandeep. P		X
25	16F81A0331	Sateesh. M	√	
26	16F81A0332	Showkiley. G		X
27	16F81A0333	Sisindri Babu. G	√	
28	16F81A0334	Sisindri. U	,	X
29	16F81A0335	Sivaji. P	√	
30	16F81A0336	Subrahmanyam. M	√	
31	16F81A0338	Sukumar. K	√	

32	16F81A0340	Uday Kiran. P	√	
33	16F81A0341	Venkatesh. P	√	
34	16F81A0343	Vinay Kumar. P	√	
35	16F81A0344	Yuvakishore. K		X
36	16F81A0345	Yuvaraju. S	,	X
37	17F85A0301	Adithya.T	√	
38	17F85A0303	Muni Rathnam.M	√	
39	17F85A0304	Naveen.N	√	
40	17F85A0305	Sai.P		X
41	17F85A0307	Venkata Naveen.CH	√	
42	15F81A0342	Sankar.k		X
43	163E1A0301	A.Yuvaraja	√	
44	163E1A0303	K.Sai	√	
45	163E1A0304	K.Balachandra		X
46	163E1A0305	K.Vineeth		X
47	163E1A0306	SK.Khadar Basha	√	
48	163E1A0307	SD.Sahid		X
49	163E1A0308	V.Mahesh	√	
50	173K5A0301	V.Manoj Kumar		X

Ble

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Gokula Krishna College of Engineering
Behind R.T.C. Depot, Sullurpet

SPSR Nellore Dt, Andhra Pradesh - 524 121

BATCH: 2018-19

	Roll	N of the Student	PASS	FAIL
S.No.	Number	Name of the Student Abbulu. N	√	
1	15F81A0301	Arafath. M		X
2	15F81A0302	Bhanuprakash. K		X
3	15F81A0303			X
4	15F81A0304	Bharath Kumar Reddy. Y Bharath Kumar. CH	√	
5	15F81A0305	Chandra Prakash. A		X
6	15F81A0307	C AUGUST CO.	√	
7	15F81A0308	Chandra Sekhar. P	√	
8	15F81A0309	Chandu. S	√	
9	15F81A0311	Chetan Kumar. T	√	
10	15F81A0312	Davood, SK	√	
11	15F81A0313	Dawood Ahmed. Sd		X
12	15F81A0315	Dilip Kumar. P		1000
13	15F81A0317	Eswar. M	√	X
14	15F81A0318	Fayaz. SK		
15	15F81A0320	Govardhan. K	V	
16	15F81A0321	Harikrishna. A		X
17	15F81A0322	Hariprasad. R		X
18	15F81A0323	Hemanth. V		X
19	15F81A0324	Krishna yadav. S	200	X
20	15F81A0325	Madhu. A	√	
21	15F81A0327	Mahesh. B		X
22	15F81A0328	Mahesh. CH		X
23	15F81A0329	Mahesh. P	√	
24	15F81A0330	Mahesh. T	√	
25	15F81A0331	Mansur Ali. M		X
26	15F81A0332	Muni Bhaskar. T	√	
27	15F81A0333	Nanda Kumar. V		X
28	15F81A0334	Naveen. B	√	
29	15F81A0335	Naveen. CH		X
30	15F81A0336	Nishanth. M		X
31	15F81A0337	Pavan Kalyan. N		X
32	15F81A0338	Prasanna Kumar. CH	√	
33	15F81A0339	Carrier and Art Burg		X
34	15F81A0341	Saikrishna. P		X
35	15F81A0341			X

36	15F81A0345	Sreekanth. CH		X
37	15F81A0346	Srikanth. S	7/84	X
38	15F81A0347	Subramanyam. R		
39	15F81A0348	Sujith Kumar. A	√	
40	15F81A0350	Surya Vamsi. B	√	
41	15F81A0351	Thameem. Sd		X
42	15F81A0353	Trivikram. S	√	
43	15F81A0354	V S Chiranjeevi. D		X
44	15F81A0355	Venkatesh. M		X
45	15F81A0356	Venkateswara Rao. T		X
46	15F81A0357	Yugandhar. M	√	
47	16F85A0301	Chandra Sekhar. B		X
48	16F85A0302	Prasanth Kumar. B		X
49	16F85A0303	Sharif. SK	√	
50	16F85A0304	Venkata Athish. G	√	
51	16F85A0305	Venkatesh. D	√	
52	16F85A0306	Vinod Kumar. S	√	
53	16F85A0307	Yeswanth. D	√	
54	14F81A0314	Kishore. M		X
55	14F81A0305	Dhanasekhar. N		X
56	153E1A0301	V.Ramu		X
57	153E1A0302	K.Sai Kiran	√	
58	153E1A0303	N.Uday Kumar		X
59	153E1A0304	CH. Lokesh Babu		X

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Gokula Krishna College of Engineering
Behind R.T.C. Depot, Sullurpet
SPSR Nellore Dt, Andhra Pradesh - 524 121



(Affiliated to JNTUA Ananthapuramu and approved by AICTE, New Delhi)

Department of Mechanical Engineering

BATCH: 2017-2018

S.No	Roll Number	Name of the Student	PASS	FAIL
1	14F81A0301	Audhithya. U		
2	14F81A0303	Chandra Sekhar. J		X
3	14F81A0304	Chenchukumar. S	Chenchukumar. S	
4	14F81A0306	Dileep Kumar. G		X
5	14F81A0307	Dilli. K	. ✓	
6	14F81A0309	Gnanasekhar. G	√	
7	14F81A0310	Gowtham. P	√	
8	14F81A0311	Hasheem. M	2/	
9	14F81A0312	Janardhan. Ch ✓		
10	14F81A0313	Kamalnadh. S		X
11	14F81A0315	Lakshmaiah. B	√	
12	14F81A0316	Lokesh. K	-/	
13	14F81A0317	Mogili Adishesaiya	√	
14	14F81A0318	Muni Kumar. J	√	
15	14F81A0320	Partheep. P		X
16	14F81A0321	Pavan Kumar. P	√	9
17	14F81A0323	Pradeep Kumar. M		X
18	14F81A0324	Pranay. P	Pranay. P	
19	14F81A0325	Prem Kumar. K		X
20	14F81A0326	Ranjith Kumar. U		X
21	14F81A0327	Raviteja. K		x

22	14F81A0328	Sai. K		X
23	14F81A0330	Santhosh Reddy. P		X
24	14F81A0331	Shahul Hameed. V.M	√	
25	14F81A0332	Srinath. Y		x
26	14F81A0333	Srinivasulu. G	√ .	
27	14F81A0334	Sujan Kumar. E		X
28	14F81A0335	Sukumar. N	√	
29	14F81A0336	Surya Teja. C	√	
30	14F81A0337	Surya Teja. R	√	
31	14F81A0338	Thirumala. K	√	
32	14F81A0339	Thulasiram. V		X
33	14F81A0340	Vamsi. B	√	
34	14F81A0341	Vamsi. R	√	
35	14F81A0342	Yugandhar. O	√ .	
36	13F81A0309	Jagadeeswar .K	√	
37	13F81A0315	MAHESH .CH		X
38	15F85A0301	Jagadeesh N		X
39	15F85A0302	Janivikranth .G		X
40	15F85A0303	Madhubabu.J	√	
41	15F85A0304	Vamsi .S	V	
42	15F85A0305	Venkateswarlu.P	√	

Selve

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Gokula Krishna College of Engineering
Behind R.T.C. Depot, Sullurpet
SPSR Nellore Dt, Andhra Pradesh - 524 121



(Affiliated to JNTUA Ananthapuramu and approved by AICTE, New Delhi)

Department of Mechanical Engineering

BATCH:2021-22



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR ANANTHAPURAMU - 515002(A.P) - INDIA

PC CMM Application Acknowledgement

Important Note: Please check carefully the spellings of Student Name, Father's Name, Mother's Name before uploading the same

Date of Application	20-07-2022
College Name	GKCE-SULLURPET
HallTicket Number	19F85A0306
Student Name	CHINTHAGINJALA HEMANTH KUMAR
Aadhaar Number	596543502727
Father Name	CHINTHAGINJALA PRASAD
Mother Name	CHINTHAGINJALA SAILAJA
Email ID	HEMANTHCHINTHAGINJALA@GMAIL.COM
Mobile Number	9390682905
Gender	Male

Note: Student should get this acknowledgement attested by your college principal along with this application and submit the same copy to the university and collect the PC CMM 30-07-2022 onwards.

Principal

PRINCIPAL

Golwis Krishna College of Engineering
Behind R.T.C. Depot, Sullurpet

SR Neliore Dt, Andhra Pradesh - \$24 121

Seal & Signature



(Affiliated to JNTUA Ananthapuramu and approved by AICTE, New Delhi)

Department of Mechanical Engineering

BATCH:2020-21

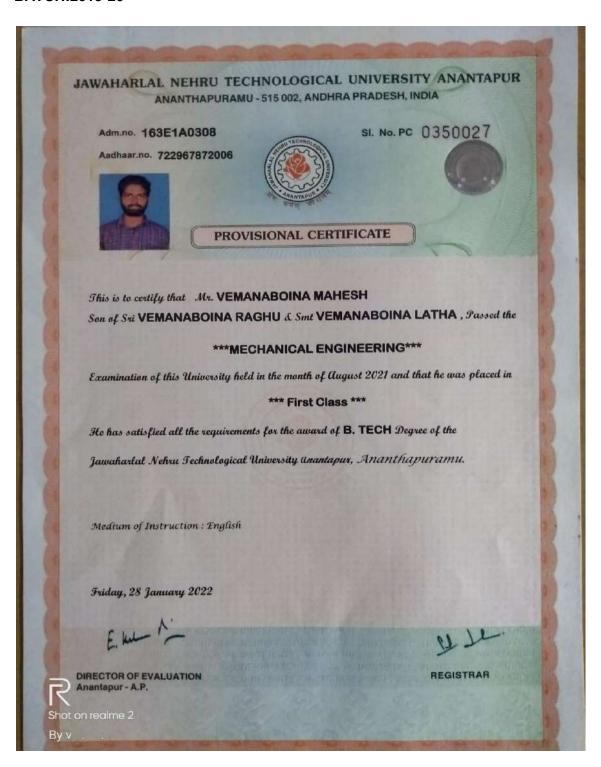




(Affiliated to JNTUA Ananthapuramu and approved by AICTE, New Delhi)

Department of Mechanical Engineering

BATCH:2019-20

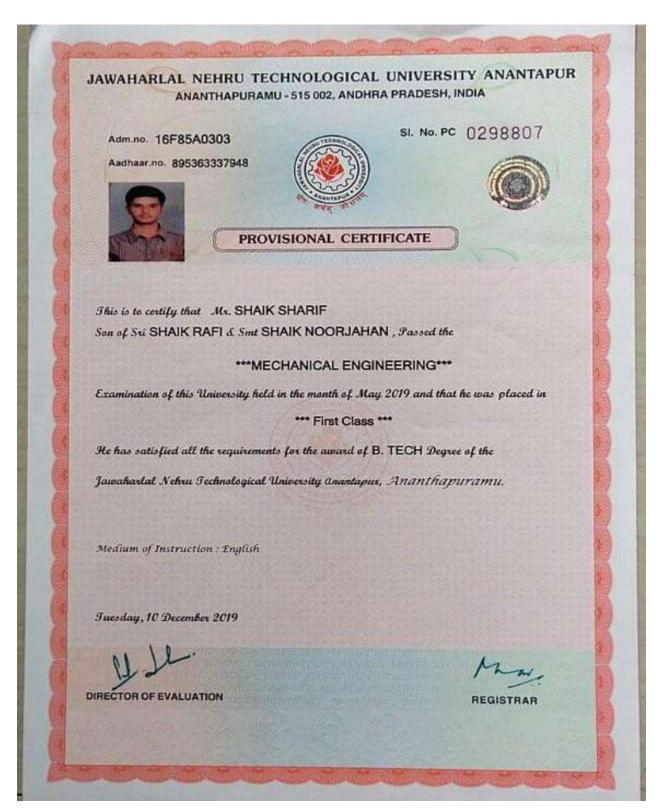




(Affiliated to JNTUA Ananthapuramu and approved by AICTE, New Delhi)

Department of Mechanical Engineering

BATCH:2018-19

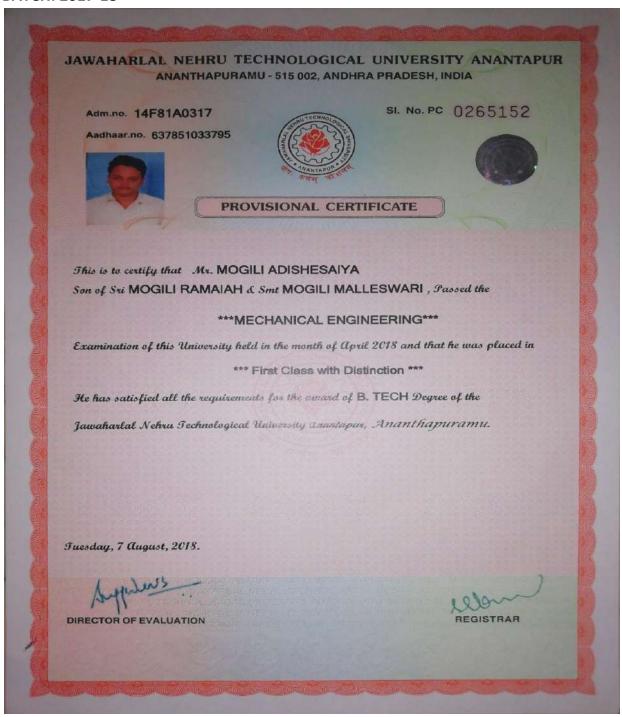




(Affiliated to JNTUA Ananthapuramu and approved by AICTE, New Delhi)

Department of Mechanical Engineering

BATCH: 2017-18



Department of Electronics and Communication Engineering Results Analysis

BATCH 2021-22

S.No	Register Number	Name of the Student	PASS	FAIL
1	18F81A0402	M.DEEPTHI	~	
2	18F81A0404	M.LAKSHMI PRASANNA	~	
3	18F81A0405	B.MALATHI		X
4	18F81A0408	T.MUNEMMA		×
5	18F81A0409	T.PRADEEP		X
6	18F81A0410	G.RAGHAVENDRA		×
7	18F81A0413	M.SHINY GEETHIKA		×
8	18F81A0415	Y.SWATHI	'	
9	18F81A0416	S.SWETHA	~	
10	18F81A0417	S.VANI	-	
11	18F81A0418	D.VENKATESH		X
12	18F81A0419	K.VIJAY		×
13	18F81A0420	RVINAY		×
14	19F85A0401	KCHANDINI		×
15	19F85A0403	M.MADHAV		X
16	18F85A0404	S.SAI SRUTHILAYA	~	
17	17F81A0440	K.SUMANTH		X

BATCH 2020-21

S.No	Register Number	Name of the Student	PASS	FAIL
1	17F81A0401	AKHILA. P		×
2	17F81A0402	ANITHA. D	~	
3	17F81A0403	ANUSHA. S	*	
4	17F81A0404	APARNA. M		×
5	17F81A0405	ASWINI. P	/	
6	17F81A0406	BALA SUBRAMANYAM. B		×
7	17F81A0407	BHARGAVI. CH		X
8	17F81A0408	BHARGAVI. P	Y	
9	17F81A0409	BHARGAVI. V	Y	
10	17F81A0410	BINDUPRIYA.CH	~	
11	17F81A0411	DILEEP. C	~	
12	17F81A0412	GIREESH KUMAR. M		×
13	17F81A0413	HARATHI.N		X
14	17F81A0414	JASHMITHA. M		×
15	17F81A0415	JAYA SURYA. J		×
16	17F81A0416	KALPANA. G		X

17	17F81A0417	KIRAN. G	~	
18	17F81A0418	LOKANADHAM. K		X
19	17F81A0419	MANASA. J	-	
20	17F81A0420	MANISHA. V		X
21	17F81A0421	MASTHANAIAH. T		X
22	17F81A0422	MEGHANA.K	V	
23	17F81A0423	NAGAMMA. K	~	
24	17F81A0424	PRAVALIKA. K	~	
25	17F81A0425	RAJASEKHAR. M	4	
26	17F81A0426	RAVIKUMAR. M	-	
27	17F81A0427	RESHMA. SK		X
28	17F81A0428	REVATHI.K		X
29	17F81A0429	SANGHAVI.M		X
30	17F81A0430	SASI KUMAR.CH	*	
31	17F81A0431	SIREESHA.S		X
32	17F81A0432	SIVA PRIYA. J	-	
33	17F81A0434	SREELEKHA. K	~	
34	17F81A0435	SRILATHA. M	-	
35	17F81A0436	SUDHA KUMARI. P		X
36	17F81A0437	SUDHAKAR. P	~	
37	17F81A0438	SUMA. A		X
38	17F81A0439	SUMA. K	~	
39	17F81A0441	SUPRAJA.D	*	
40	17F81A0442	SUREKHA.M	*	
41	17F81A0443	SURYAVARMA.N	*	
42	17F81A0444	SUSMITHA. R	~	
43	17F81A0445	SWARNA MALYA. V	*	
44	17F81A0446	TEJASWINI. T	*	
45	17F81A0447	THOYAJA. N	4	
46	17F81A0448	USHA. Y		X
47	17F81A0449	VAMSI. M		X
48	17F81A0450	VANI.E		X
49	17F81A0451	VISWANTH KUMAR. CH	*	
50	18F85A0401	DEVANTH KUMAR.N		X
51	18F85A0403	KIRAN. CH	*	
52	16F81A0442	SIREESHA.P		X

BATCH 2019-20

S.No	Register Number	Name of the Student	PASS	FAIL
1	16F81A0401	Kadiri Aswini	~	
2	16F81A0403	Shaik Daood		X
3	16F81A0404	Arambakam devi Prasad	~	
4	16F81A0405	Ummiti Dhanush		×
5	16F81A0406	Lingareddy Dillikumari	~	
6	16F81A0407	Kanna Dinesh Kumar		×

7	16F81A0408	Mamidi Gopi Bharthkumar		X
8	16F81A0409	Aundagundala Harichandana		Ŷ
9	16F81A0410	Bunga Haritha	-	_
10	16F81A0411	Kotapati Harshitha	-	
11	16F81A0412	Polam Reddy Jhansi		X
12	16F81A0414	Gedi Kavitha	-	^
13	16F81A0416		-	-
14	16F81A0417	Kongamudi Lakshmipathi Puttu Malliswari	-	X
15	16F81A0418	Gurakala Mamatha	-	X
16	16F81A0419			×
17		Shaik Mobina		×
18	16F81A0420	Batta Mounica		
19	16F81A0421	Vetti Mounica	Y	
	16F81A0423	Satyaveti Nagaharichandana	Y	_
20	16F81A0425	Kalleti Nandini	Y	
21	16F81A0426	Perisetla Padmaja		X
22	16F81A0427	Ramabatini Pavan		X
23	16F81A0428	Suluru Pavankalyan		X
24	16F81A0429	Nandrambakkam Pavithra		×
25	16F81A0430	Parangi Polamma	*	
26	16F81A0431	Pooja.Ls		X
27	16F81A0432	Palamani Praavalika	~	
28	16F81A0433	Mahimaluri.Pravallika		X
29	16F81A0434	Neelam.Pravallika	~	
30	16F81A0435	Udathanapalli.Ramya	~	
31	16F81A0436	Kumari. Sai Krishna		X
32	16F81A0438	Yaragala.Sandya	V	
33	16F81A0439	Pinne.Selvi		X
34	16F81A0440	Chintapudi.Shalini	~	
35	16F81A0441	Golla Silpa	~	
36	16F81A0443	Banadaru.Sivakumar		×
37	16F81A0444	Kayyala.Sreelatha	~	
38	16F81A0445	Alluru.Sudeepthi	~	
39	16F81A0447	Obbu.Sunil		X
40	16F81A0449	Prudhvi.Vandana	~	
41	16F81A0450	Guggilla.Venkat Sai Krishna		X
42	16F81A0451	Chinnasetty.Vijay		X
43	16F81A0452	Gedi .Vinod		×
44	16F81A0453	Prudhvi.Vishnu		X
45	16F81A0454	Bairabathana.Yamuna	-	
46	16F81A0455	Veeramreddy.Yamuna		×
47	17F85A0401	Jayasree.V	-	
48	17F85A0403	Naresh.K		×
	1/roamu4ua			
49	17F85A0404	Ramya.S		

BATCH 2018-19

S.No	Register Number	Name of the Student	PASS	FAIL
1	15F81A0401	Asha. G	~	
2	15F81A0402	Bhanuprakash. A		X
3	15F81A0403	Bhuvaneswari. T		X
4	15F81A0404	Chandana. E	~	
5	15F81A0406	Chengamma. P	~	
6	15F81A0407	Deekshitha. G	Y	
7	15F81A0408	Devisree. G	~	
8	15F81A0409	Divya. S	*	
9	15F81A0410	Gayathri. B	~	
10	15F81A0411	Geetika. N	~	
11	15F81A0414	Harshitha. D	~	
12	15F81A0415	Jyothi. V	*	
13	15F81A0416	Kavya. B	~	
14	15F81A0417	Laila. K	~	
15	15F81A0418	Lakshmi Narayanamma. K	~	
16	15F81A0419	Lavanya. V		×
17	15F81A0421	Meghana. M		X
18	15F81A0422	Mohan. P		×
19	15F81A0423	Monasree. S	~	
20	15F81A0424	Mounika. A		×
21	15F81A0425	Mounika. M	~	
22	15F81A0426	Mounika. V	~	
23	15F81A0428	Muni Subramanyam. B	~	
24	15F81A0429	Munisha. V	~	
25	15F81A0430	Niharika. N	~	
26	15F81A0431	Nikhila. G	~	
27	15F81A0432	Pavani. N	~	
28	15F81A0433	Praveena. K	~	
29	15F81A0434	Pushpalatha.S	~	
30	15F81A0435	Puvvaras. AEI		×
31	15F81A0436	Raghavulu. P	✓	
32	15F81A0437	Reshma. M	~	
33	15F81A0438	Saipreethi. M	~	
34	15F81A0439	Sandhya. A		X
35	15F81A0440	Siva Sai. A	~	
36	15F81A0441	Spandana. M	~	
37	15F81A0442	Thanuja. V	~	
38	15F81A0444	Thulasi. P	~	
39	15F81A0445	Uma. A	~	
40	16F85A0402	Pavithra. K	~	
41	16F85A0403	Sailesh Kumar. K	~	
42	16F85A0404	Vijitha. J	~	

BATCH 2017-18

S.No	Register Number	Name of the Student	PASS	FAIL
1	14F81A0401	Alekya. P	~	
2	14F81A0402	Bhupaiah. B	~	
3	14F81A0403	Chandana. M		×
4	14F81A0404	Chandana. S		×
5	14F81A0405	Chandana. V	*	
6	14F81A0406	Chandini. G	~	
7	14F81A0408	Dimple. K		×
8	14F81A0409	Guravaiah. G		X
9	14F81A0410	Guravaiah. N		×
10	14F81A0411	Kiran Yadav. B		×
11	14F81A0413	Manju. Ch	~	
12	14F81A0415	Mounika. C	~	
13	14F81A0416	Mounika. K	~	
14	14F81A0417	Nagamani. S		×
15	14F81A0418	Nishitha. E	~	
16	14F81A0419	Ramya. M		×
17	14F81A0420	Rekha. J		×
18	14F81A0421	Sangeetha. T	~	
19	14F81A0422	Shobha. S	~	
20	14F81A0423	Shyni. V		×
21	14F81A0424	Silpa. K		×
22	14F81A0425	Suma. G		×
23	14F81A0427	Yamini. V	~	
24	15F85A0401	Chandrakala.S	~	
25	15F85A0402	Hari Prasad.M		×
26	15F85A0403	K. Muni Lakshmi Bharathi	~	
27	15F85A0404	Nasreen. SK		×
28	15F85A0405	Sai Kusuma Latha .I	~	
29	15F85A0406	Sobha Rani. A	~	
30	15F85A0407	Venkata Krishnaiah.B	~	
31	13F81A0414	Hemanth. K		X
32	14F85A0401	Anil. G		X

M. Chy S.

Dept. Of Electronics & Comm.

Gokula Krishna College of 160.,
Sullurpet-524 121, Nellore Dt., A.P.

PRINCIPAL

PRINCIPAL

Gokula Krishna College of Enginee.

Behind R.T.C. Depot, Sullurpet

Sost Nellore Dt, Andhra Pradesh - 524 121

Adm.no. 14F81A0421

Aadhaar.no. 762345261490



SI. No. PC 0273545







PROVISIONAL CERTIFICATE

This is to certify that Mo. THOTA SANGEETA Daughter of Sri T SUBBARAO & Smt T SRI GOWRI, Passed the

ELECTRONICS & COMMUNICATION ENGINEERING

Examination of this University held in the month of April 2018 and that she was placed in *** First Class with Distinction ***

She has satisfied all the requirements for the award of B. TECH Degree of the

Jawaharlal Nehru Technological University anantapur, Ananthapuramu.

Tuesday, 20 November 2018

DIRECTOR OF EVALUATION

Adm.no. 15F81A0428

Aadhaar.no. 462072657855





SI. No. PC 0287780



PROVISIONAL CERTIFICATE

This is to certify that Mr. BEESABATHINA MUNI SUBRAMANYAM
Son of Sri BEESABATHINA GOVARDHAN & Smt BEESABATHINA KANCHANA,
Passed the

ELECTRONICS & COMMUNICATION ENGINEERING

Examination of this University held in the month of April 2019 and that he was placed in

*** First Class with Distinction ***

He has satisfied all the requirements for the award of B. TECH Degree of the Jawaharlal Nehru Technological University Anantapur, Ananthapuramu.

Medium of Instruction : English

Tuesday, 23 July 2019

DIRECTOR OF EVALUATION

Adm.no. 16F81A0432

Aadhaar.no. 864985509856



SI. No. PC 0312519



PROVISIONAL CERTIFICATE

This is to certify that Ms. PALAMANI PRAAVALIKA

Daughter of Sri P.SIVASANKAR & Smt P.BUJJAMMA, Passed the

ELECTRONICS & COMMUNICATION ENGINEERING

Examination of this University held in the month of September 2020 and that she was placed in

*** First Class with Distinction ***

She has satisfied all the requirements for the award of B. TECH Degree of the

Jawaharlal Nehru Technological University anantapur, Ananthapuramu.

Medium of Instruction : English

Thursday, 12 November 2020

DIRECTOR OF EVALUATION

REGISTRAR

Note: - All UG Final Semester examinations for the academic year 2019-2020 were originally scheduled in the month of April 2020.

But, in view of Covid-19 and as per UGC guidelines these examinations were conducted in the month of September 2020.

Adm.no. 17F81A0445

Aadhaar.no. 959103518761





SI. No. PC 0344360



PROVISIONAL CERTIFICATE

This is to certify that Ms. VELKUR SWARNA MALYA

Daughter of Sri VELKUR SREENIVASULU & Smt VELKUR LATHA, Passed the

ELECTRONICS & COMMUNICATION ENGINEERING

Examination of this University held in the month of July 2021 and that she was placed in

*** First Class with Distinction ***

She has satisfied all the requirements for the award of B. TECH Degree of the

Jawaharlal Nehru Technological University anantapur, Ananthapuramu.

Medium of Instruction: English

Wednesday, 24 November 2021

E. W.

DIRECTOR OF EVALUATION Anantapur - A.P.

TTT.

Department of Computer Science and Engineering

Result Analysis

BATCH: 2021-22

SNO	ROLL NO	STUDENT NAME	PASS	FAIL
1	18F81A0501	CHINAGA ANU TEJASWI	\checkmark	
2	18F81A0502	NELAVALA ANUSHA	√	
3	18F81A0503	TIRAKALA BHARGAVI		×
4	18F81A0504	DOODI DINESH KUMAR		×
5	18F81A0506	DUVVURU LIKITH SAIKUMAR		×
6	18F81A0507	PANNEM MANJULA	√	
7	18F81A0509	SYED NASRIN		×
8	18F81A0510	VEMPALLI PAVANCHAND		×
9	18F81A0511	ANAMALA PUSHPA RAJ		×
10	18F81A0512	PEDAPAPU SAIROHITH		×
11	18F81A0513	SIRISANAGANDLA SAIVISESH		×
12	18F81A0514	GUNTURU SANDEEP		×
13	18F81A0515	PEDDAGARI SANKAR		×

14	18F81A0516	MERIMU SRINIVASULU		×
15	18F81A0517	BATTA SUKUMAR		×
16	18F81A0518	VUNNAM TEJASWINI REDDY		×
17	18F81A0519	BILLU UDAYABHANU	✓	
18	18F81A0520	KOVI UNNATH		×
19	18F81A0522	VEERATHURU VIDYA	√	
20	18F81A0523	GANAPATHI VINEELA	√	
21	16F81A0514	KUDIRI DEVI		×
22	17F81A0534	SHAIK SHAKEER		*
23	17F81A0543	SAMUDRALA TEJASWI		×

BATCH: 2020-21

SNO	ROLL NO	STUDENT NAME	PASS	FAIL
1	17F81A0501	ABHISHEK . P		×
2	17F81A0502	ANILKUMAR. N		×
3	17F81A0503	ARUN KUMAR . S		×
4	17F81A0504	BHANUPRAKASH. P	✓	
5	17F81A0505	BHARGAV. K		×

6	17F81A0506	BHARGAVI . V		×
7	17F81A0507	BHAVANA . K	✓	
8	17F81A0508	CHAYA KIRANMAYEE REDDY		×
9	17F81A0509	DAYA SAGAR . A		×
10	17F81A0510	DEVI . K	✓	
11	17F81A0511	DIVYA . P	✓	
12	17F81A0512	HARATHI . C	√	
13	17F81A0514	JYOTHSANA . S		×
14	17F81A0515	KAAVYA . M	√	
15	17F81A0517	KAVYA. M	√	
16	17F81A0519	MAMATHA . B		×
17	17F81A0520	MANEMMA . B		×
18	17F81A0521	MANIKANTA . G		×
19	17F81A0522	MANOJKUMAR. M	√	
20	17F81A0523	NAVEEN KUMAR. N		×
21	17F81A0524	PAVAN SAI. K		×
22	17F81A0526	PRATHYUSHA . G		×
23	17F81A0527	PREM SAI . M		×

24	17F81A0528	PRIYANKA. G		×
25	17F81A0529	RAHULKUMAR. M		×
26	17F81A0530	SADHANA REDDY. G	✓	
27	17F81A0531	SAISATHYA . V	✓	
28	17F81A0532	SAISWARUPA . V		×
29	17F81A0533	SANDHYA . K	✓	
30	17F81A0535	SIREESHA . D		×
31	17F81A0537	SIVA KUMAR . A		×
32	17F81A0539	SREENU. R		×
33	17F81A0541	SUJANI. Y	√	
34	17F81A0542	SUKUMAR. U		×
35	17F81A0544	THULASI. M		×
36	17F81A0545	USHARANI . C	✓	
37	17F81A0546	VEDITHA. P	✓	
38	17F81A0547	VENKATA MAHESH. G		×
39	17F81A0548	VENKATA SAI . B	✓	
40	17F81A0549	VENKATA SAI NIKITHA . M	√	
41	17F81A0551	YUVARAJU. S		×

42	17F81A0552	KAVITHA.V	×
43	16F81A0529	NISHITHA .P (RA)	*

BATCH: 2019-20

SNO	ROLL NO	STUDENT NAME	PASS	FAIL
1	16F81A0501	AASHA. T		×
2	16F81A0502	AMARAVATHI. C		×
3	16F81A0503	ANCHAL SHREE		×
4	16F81A0504	ANITHA. D		×
5	16F81A0505	ANUSREE. N		×
6	16F81A0506	ARUNA. S		×
7	16F81A0507	AYESHA YASMEEN. SK	√	
8	16F81A0508	BHAGYAMMA. A		×
9	16F81A0511	CHANDU KUMAR. B		×
10	16F81A0512	CHENCHAMMA. M		×
11	16F81A0513	CHINTALAMMA. K		×
12	16F81A0515	GANESH. G		×
13	16F81A0516	HIMABINDU. N		×
14	16F81A0517	JAYANTHI. K		×

15	16F81A0520	KAVITHA. K	✓	
16	16F81A0521	KEERTHANA. G		×
17	16F81A0522	LYDIA. T		×
18	16F81A0523	MADHAVA. G		×
19	16F81A0524	MANASA. N		×
20	16F81A0525	MERY. N		×
21	16F81A0526	MUNI KRISHNA. P		×
22	16F81A0527	NAGAJYOTHI. Y		×
23	16F81A0528	NANDINI. P	√	
24	16F81A0530	PADMA PRIYA. B		×
25	16F81A0531	PAVANI. A		×
26	16F81A0532	PRAMEELA. P	√	
27	16F81A0533	PRAVALLIKA. G		×
28	16F81A0536	SAIKRUPAKAR. K	√	
29	16F81A0538	SARITHA. A		×
30	16F81A0539	SHALINI. E		×
31	16F81A0540	SRAVANI. B	√	
32	16F81A0541	SRAVANI. KATURI		×

33	16F81A0542	SRAVANI. KONDURU	×
34	16F81A0543	SREE LAKSHMI. Y	×
35	16F81A0544	SUPRIYA. B	×
36	16F81A0545	SWARUPARANI. K	×
37	16F81A0547	THANMAI. C	✓
38	16F81A0548	VAMSIBABU. P	×
39	16F81A0549	YUVAMANIKANTA. K	×
40	16F81A0550	RAKESH.Y	×
41	17F85A0501	JHANSI.S	×
42	17F85A0502	SUHASINI.V	×

BATCH: 2018-19

ROLL NO	STUDENT NAME	PASS	FAIL
15F81A0501	S. AJAY KUMAR		×
15F81A0502	V. ANITHA		×
15F81A0504	V. GAYATHRI	✓	
15F81A0505	M. GIRI	✓	
15F81A0506	K. GIRIJA		×
15F81A0507	V. GOPI		×
	15F81A0501 15F81A0502 15F81A0504 15F81A0505	15F81A0501 S. AJAY KUMAR 15F81A0502 V. ANITHA 15F81A0504 V. GAYATHRI 15F81A0505 M. GIRI 15F81A0506 K. GIRIJA	15F81A0501 S. AJAY KUMAR 15F81A0502 V. ANITHA 15F81A0504 V. GAYATHRI ✓ 15F81A0505 M. GIRI ✓ 15F81A0506 K. GIRIJA

7	15F81A0508	C. HELASREE		×
8	15F81A0509	G. JHANSI		×
9	15F81A0510	P. KARISHMA KHAN		×
10	15F81A0511	T. KARISHMA	√	
11	15F81A0512	K. LAKSHMI	√	
12	15F81A0513	P. LOKESH		×
13	15F81A0514	G. MOUNIKA		×
14	15F81A0516	K.RAMACHANDRA		×
15	15F81A0518	P. SAI KRISHNA		×
16	15F81A0519	B.SRILEKHA	✓	
17	14F81A0506	P. HARINI		*

BATCH: 2017-18

SNO	ROLL NO	STUDENT NAME	PASS	FAIL
1	14F81A0501	M. ABHISHEK		×
2	14F81A0503	K. ANUSHA		×
3	14F81A0507	V. HARITHA		×
4	14F81A0508	Y. HIMAJA		×
5	14F81A0509	T. LAKSHMI SAI SREE	√	

6	14F81A0510	B. LEELA VARA PRASAD		×
7	14F81A0511	M. MADHU BALA		×
8	14F81A0513	B. MOUNIKA	√	
9	14F81A0514	Y. NAGA PRAVALLIKA		×
10	14F81A0516	M. RAMYA		×
11	14F81A0517	T. SAI GAYAYTHRI		×
12	14F81A0518	K. SANDHYA	√	
13	13F81A0518	RUKUSH		×
14	13F81A0517	A. RESHMA		×
15	12381A0526	K. SAI PRADEEP		*

BATCH: 2016-17

SNO	ROLL NO	STUDENT NAME	PASS	FAIL
1	13F81A0501	A. SARAHMONIKA	√	
2	13F81A0502	K. ANIL KUMAR	✓	
3	13F81A0503	K .ARAVINDA REDDY		×
4	13F81A0504	A. BRUNDHA		×
5	13F81A0505	G. EESWARAIAH		×
6	13F81A0506	SK. FAZUL		×
7	13F81A0508	P. JAYA KISHORE		×
8	13F81A0509	S. KEERTHI		×
9	13F81A0510	K. MAHALAKSHMI	√	
10	13F81A0511	A. MANJEERA KUMARI		×
11	13F81A0512	A. PALLAVI PRIYA		×
12	13F81A0514	T. PRASANTH		×
13	13F81A0515	T. PRAVEENA		*
14	13F81A0516	M. RANJITH		×
15	13F81A0519	G. SAI DIVYA SRI		×

16	13F81A0521	A. SANGEETHA		*
17	13F81A0523	V. SRILEKHA		×
18	13F81A0524	A. SUKANYA		×
19	13F81A0525	N. SUMALATHA		×
20	13F81A0529	CH .VIJITHA	√	
21	14F85A0502	A. PRIYANKA	✓	
22	12F81A0505	M. DEEPTHI KRISHNA	√	
23	13R61A0548	SYED HAJIRA	√	

BATCH: 2020-21

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR ANANTHAPURAMU - 515 002, ANDHRA PRADESH, INDIA

Adm.no. 17F81A0533

Aadhaar.no. 638602192716



SI. No. PC 0334819



PROVISIONAL CERTIFICATE

This is to certify that Ms. KONDA SANDHYA

Daughter of Sri KONDA KRISHNAIAH & Smt KONDA MUNENDRAMMA, Fassed the

COMPUTER SCIENCE & ENGINEERING

Examination of this University held in the month of July 2021 and that she was placed in

*** First Class with Distinction ***

She has satisfied all the requirements for the award of B. TECH Degree of the

Jawaharlal Nehru Technological University anantapur, Ananthapuramu.

Medium of Instruction : English

Monday, 23 August 2021

E. W.

DIRECTOR OF EVALUATION
Anantapur - A.P.

Myr.

Adm.no. 17F81A0545

Aadhaar.no. 697768232634



SI. No. PC 0334823





PROVISIONAL CERTIFICATE

This is to certify that Ms. CHEEKATI USHARANI

Daughter of Sri CH SUBRAMANYAM & Smt CH SUJATHA, Passed the

COMPUTER SCIENCE & ENGINEERING

Examination of this University held in the month of July 2021 and that she was placed in

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Jawaharlal Nehru Technological University anantapur, Ananthapuramu.

Medium of Instruction: English

Monday, 23 August 2021

DIRECTOR OF EVALUATION Anantapur - A.P. ATT

Adm.no. 17F81A0511 Aadhaar.no. 496436151160



SI. No. PC 0334813





PROVISIONAL CERTIFICATE

This is to certify that Ms. PITTI DIVYA

Daughter of Sri PITTI BALAIAH & Smt PITTI JAYALALITHA, Fassed the

COMPUTER SCIENCE & ENGINEERING

Examination of this University held in the month of July 2021 and that she was placed in

*** First Class with Distinction ***

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Medium of Instruction: English

Monday, 23 August 2021

E. W.

DIRECTOR OF EVALUATION Anantapur - A.P.

77 Tr

Adm.no. 17F81A0508

Aadhaar.no. 316575821847





SI. No. PC 0348544



PROVISIONAL CERTIFICATE

This is to certify that Ms. GADDAM CHAYA KIRANMAYEE REDDY

Daughter of Sri GADDAM SANTHI PRIYA & Smt GADDAM USHARANI, Passed the

COMPUTER SCIENCE & ENGINEERING

Examination of this University held in the month of August 2021 and that she was placed in

*** First Class with Distinction ***

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Medium of Instruction: English

Tuesday, 4 January 2022

DIRECTOR OF EVALUATION
Anantapur - A.P.

Adm.no. 17F81A0544

Aadhaar.no. 514738500197



SI. No. PC 0334822





PROVISIONAL CERTIFICATE

This is to certify that Mo. MUPPALLA THULASI

Daughter of Sri MUPPALLA NARAHARI RAJU & Smt MUPPALLA NIRMALA, Fassed the

COMPUTER SCIENCE & ENGINEERING

Examination of this University held in the month of July 2021 and that she was placed in

*** First Class with Distinction ***

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Jawaharlal Nehru Technological University anantapur, Ananthapuramu.

Medium of Instruction: English

Monday, 23 August 2021

E.W.

DIRECTOR OF EVALUATION Anantapur - A.P. 77 75